

**Project Report  
ATC-32**

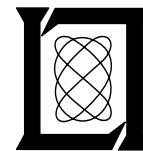
# **The Effect of Phase Error on the DPSK Receiver Performance**

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4 February 1974

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Prepared for the Federal Aviation Administration,  
Washington, D.C. 20591.

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1. Report No. FAA-RD-74-21	2. Government Accession No.	3. Recipient's Catalog No.	
4. Title and Subtitle The Effect of Phase Error on the DPSK Receiver Performance		5. Report Date 4 February 1974	
		6. Performing Organization Code	
7. Author(s) D.A. Shnidman		8. Performing Organization Report No. ATC-32	
9. Performing Organization Name and Address Massachusetts Institute of Technology Lincoln Laboratory P.O. Box 73 Lexington, Massachusetts 02173		10. Work Unit No (TRIS) 45364 Project No. 034-241-012	
		11. Contract or Grant No. IAG-DOT-FA72WAI-261	
		13. Type of Report and Period Covered Project Report	
12. Sponsoring Agency Name and Address Department of Transportation Federal Aviation Administration Systems Research and Development Service Washington, D.C. 20591		14. Sponsoring Agency Code	
15. Supplementary Notes The work reported in this document was performed at Lincoln Laboratory, a center for research operated by Massachusetts Institute of Technology under Air Force Contract F19628-73-C-0002.			
16. Abstract  Several methods of realizing a DPSK receiver use a delay line. Temperature variations cause changes in the delay which, in turn, cause errors in the phase differences between the reference and information signals. The effect of these errors on the performance of an optimum DPSK receiver is studied in this report.			
17. Key Words  DPSK Delay Error		18. Distribution Statement  Document is available to the public through the National Technical Information Service, Springfield, Virginia 22151	
19. Security Classif. (of this report) Unclassified	20. Security Classif. (of this page) Unclassified	21. No. of Pages 26	22. Price 3.00 HC 1.45 MF

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SECTION 1  
INTRODUCTION

Several methods of realizing a DPSK receiver use delay lines. Errors in the delay cause a phase difference error,  $\Delta$ , between the reference and information pulses. The delay can be adjusted at any given temperature but, since the delay line is temperature sensitive and the receiver is subject to a range of temperatures, phase errors are likely to arise. The effect of these errors on the performance of the receiver is analyzed in this report.

Represented in Figure 1 is the design of an optimum receiver. The delay  $\tau$  is equal to  $T + \epsilon$  where  $\epsilon$  is the delay error. The output of the mixer has a phase error  $\Delta$ .

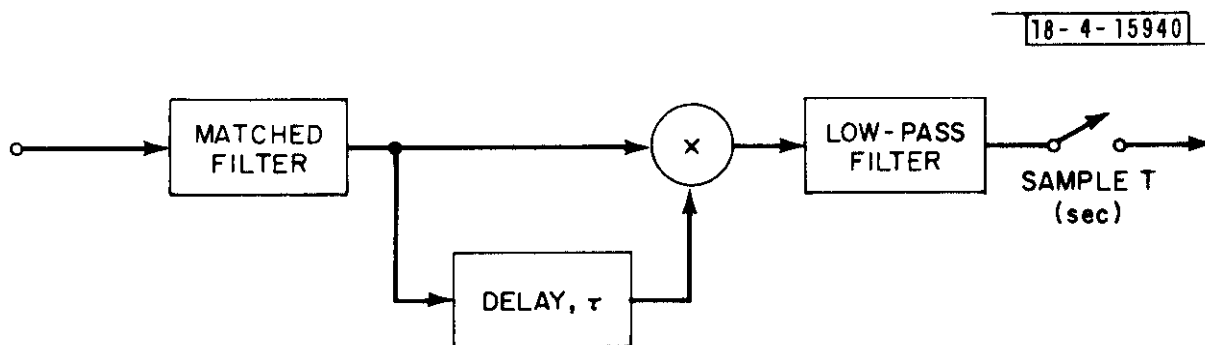


Figure 1. Realization of DPSK Receiver.

$$\Delta \text{ (rad)} = 2\pi F_C \epsilon \quad (1)$$

or

$$\Delta \text{ (deg)} = 360 F_C \epsilon \quad (2)$$

where  $F_C$  is the carrier frequency of the input to the matched filter. At an IF frequency of 60 MHz we get

$$\Delta \text{ (deg)} = 21.6 \epsilon \text{ (nsec)} \quad (3)$$

Table 1 presents  $\Delta$  in degrees vs  $\epsilon$ . The effect of  $\Delta$  on  $P_e/\text{bit}$  is analyzed below and limits on the range of  $\Delta$  are determined.

Table 1.  $\Delta$  (degrees) vs.  $\epsilon$  (nsec) for 60 MHz.

$\epsilon$ (nsec)	$\Delta$ (degrees)
0.5	10.8
1.0	21.6
1.5	32.4
2.0	43.2
2.5	54.0
3.0	64.8
3.5	75.6
4.0	86.4

SECTION 2  
EXACT ERROR EXPRESSION

The  $P_e$ /bit formulas for DPSK given in Project Report ATC-12 [1] do not include the parameter  $\Delta$ . It is therefore necessary to generalize the  $P_e$ /bit expressions and to accomplish this, we take a slightly different approach. First, we define the following parameters:

- $E/N_0$  is the signal-to-noise ratio.
- $\rho_1^2$  is the jamming-to-signal ratio on one of the pulse pairs.
- $\rho_2^2$  is the jamming-to-signal ratio on the other of the pulse pairs.
- $\Delta$  is the phase difference error.
- $\theta$  is the phase angle between the signal and jamming carriers.

$\rho_2 = |\rho_2|$  if the jamming pulses have the same phase relationship over the two baud intervals as do the reference and information pulses.

$\rho_2 = -|\rho_2|$  if the jamming pulses in the two baud intervals have the opposite phase relationship as do the reference and information pulses.

If we define  $P_\theta(E/N_0, \rho_1, \rho_2, \Delta, \theta)$  as the bit probability of error for a given set of values for  $E/N_0$ ,  $\rho_1$ ,  $\rho_2$ ,  $\Delta$ , and  $\theta$ , then it is shown in Appendix A that



$$P_{\theta}(E/N_0, \rho_1, \rho_2, \Delta, \theta) = \frac{1}{2}[1 - Q(\sqrt{b}, \sqrt{a}) + Q(\sqrt{a}, \sqrt{b})] \quad (4)$$

where

$$\begin{aligned} a &= a(E/N_0, \rho_1, \rho_2, \Delta, \theta) \\ &= \frac{E}{N_0} \left\{ [1 + (\rho_1 + \rho_2) \cos \theta] (1 - \cos \Delta) + \frac{\rho_1^2 - 2\rho_1 \rho_2 \cos \Delta + \rho_2^2}{2} \right. \\ &\quad \left. + (\rho_1 - \rho_2) \sin \theta \sin \Delta \right\} \end{aligned} \quad (5)$$

and

$$\begin{aligned} b &= b(E/N_0, \rho_1, \rho_2, \Delta, \theta) \\ &= \frac{E}{N_0} \left\{ [1 + (\rho_1 + \rho_2) \cos \theta] (1 + \cos \Delta) + \frac{\rho_1^2 + 2\rho_1 \rho_2 \cos \Delta + \rho_2^2}{2} \right. \\ &\quad \left. - (\rho_1 - \rho_2) \sin \theta \sin \Delta \right\} \end{aligned} \quad (6)$$

In order to obtain the  $P_e/\text{bit}$ , we must sum the two cases  $\rho_2 = |\rho_2|$  and  $\rho_2 = -|\rho_2|$  and average over the uniformly distributed variable,  $\theta$

$$P_e/\text{bit} = \frac{1}{4\pi} \int_{-\pi}^{\pi} \left[ P_{\theta} \left( \frac{E}{N_0}, \rho_1, \rho_2, \Delta, \theta \right) + P_{\theta} \left( \frac{E}{N_0}, \rho_1, -\rho_2, \Delta, \theta \right) \right] d\theta \quad (7)$$

Using Eq. (7), we generate Table 2, showing  $P_e/\text{bit}$  vs.  $\Delta$  for different  $E/N_0$  and  $\rho$ , where  $\rho_1 = |\rho_2| = \rho$ . In Figure 2, some of these results are plotted. We note that for  $\Delta > 10^\circ$ , the  $P_e/\text{bit}$  is dependent on  $\rho$  and to a much lesser extent on  $E/N_0$ . This is especially true for very large  $E/N_0$ . We can, therefore, obtain an understanding of the relationship of  $P_e/\text{bit}$  vs.  $\rho$  by letting  $E/N_0$  go to infinity. The results are presented in the next section.

Table 2.  $P_e$ /bit vs.  $\Delta$  for  $\rho = 0, 0.5, 0.8,$  and  $0.9$ .

$\rho$	$\Delta$ (degrees)	$E/N_0 \approx 16$ dB	$E/N_0 = 20$ dB	$E/N_0 \approx 25$ dB
0	0	$< 10^{-12}$	$< 10^{-12}$	$< 10^{-12}$
	10	$< 10^{-12}$	$< 10^{-12}$	$< 10^{-12}$
	20	$< 10^{-12}$	$< 10^{-12}$	$< 10^{-12}$
	30	$< 10^{-12}$	$< 10^{-12}$	$< 10^{-12}$
0.5	0	$2.8 \times 10^{-6}$	$2.5 \times 10^{-12}$	$< 10^{-12}$
	10	$5.6 \times 10^{-5}$	$7.4 \times 10^{-7}$	$7.3 \times 10^{-7}$
	20	$1.7 \times 10^{-3}$	$3.1 \times 10^{-5}$	$7.3 \times 10^{-7}$
	30	$1.8 \times 10^{-2}$	$6.0 \times 10^{-3}$	$4.3 \times 10^{-4}$
0.8	0	$2.1 \times 10^{-2}$	$1.3 \times 10^{-3}$	$2.7 \times 10^{-7}$
	10	$5.7 \times 10^{-2}$	$3.1 \times 10^{-2}$	$1.2 \times 10^{-2}$
	20	$1.4 \times 10^{-1}$	$1.4 \times 10^{-1}$	$1.4 \times 10^{-1}$
	30	$1.9 \times 10^{-1}$	$1.9 \times 10^{-1}$	$1.9 \times 10^{-1}$
0.9	0	$1.0 \times 10^{-1}$	$4.2 \times 10^{-2}$	$3.8 \times 10^{-3}$
	10	$1.5 \times 10^{-1}$	$1.4 \times 10^{-1}$	$1.4 \times 10^{-1}$
	20	$2.1 \times 10^{-1}$	$2.0 \times 10^{-1}$	$2.0 \times 10^{-1}$
	30	$2.3 \times 10^{-1}$	$2.2 \times 10^{-1}$	$2.2 \times 10^{-1}$

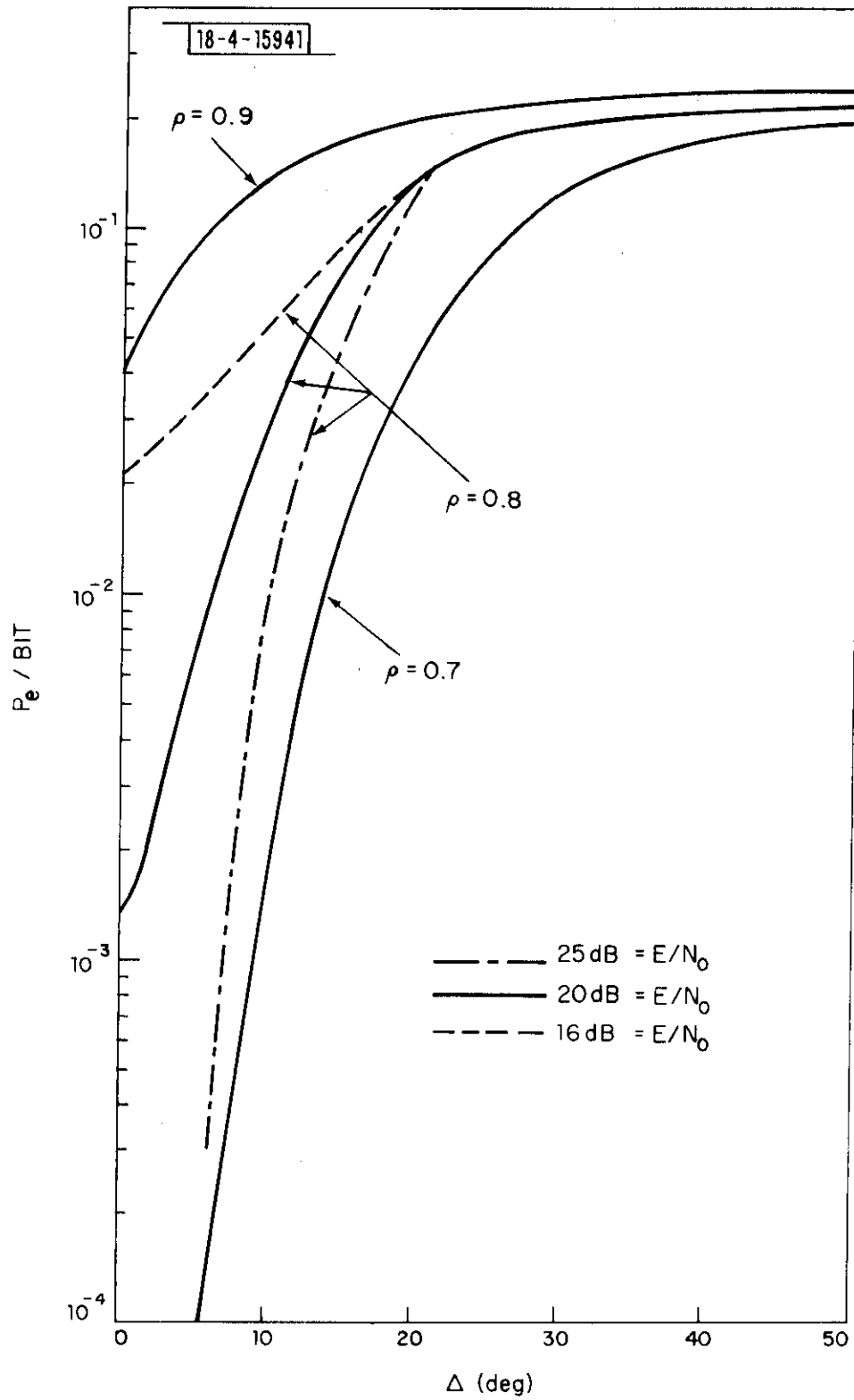


Fig. 2. Plot of  $P_e/\text{bits}$  vs  $\Delta$  for Several Values of  $\rho$  and  $E/N_0$ .

### SECTION 3

#### P<sub>e</sub>/bit FOR E/N<sub>0</sub> INFINITE

For E/N<sub>0</sub> infinite, the P<sub>e</sub>/bit will depend only on ρ and Δ. Figure 3 represents a worst-case situation for P<sub>e</sub>/bit with ρ<sub>1</sub> = ρ and ρ<sub>2</sub> = -ρ. In this case, we have an error only if Δ is larger than Δ<sub>e</sub>(θ,ρ) where

$$\Delta_e(\theta, \rho) = \frac{\pi}{2} - \psi(\theta, \rho) \quad -\pi \leq \theta \leq 0 \quad (8)$$

where, in turn, ψ(θ,ρ) (See Figure 3) is

$$\psi(\theta, \rho) = \cos^{-1} \left( \frac{1 - \rho^2}{\sqrt{1 + 2\rho^2 - 4\rho^2 \cos \theta + \rho^4}} \right) ; \quad (9)$$

that is, P<sub>e</sub>/bit is zero if Δ < Δ<sub>e</sub>(θ,ρ).

ψ(θ,ρ) is a maximum and Δ<sub>e</sub>(θ,ρ) is a minimum when θ = -π/2 so that

$$\Delta_M = \Delta_e\left(-\frac{\pi}{2}, \rho\right) = \frac{\pi}{2} - \cos^{-1} \left( \frac{1 - \rho^2}{1 + \rho^2} \right) \quad (10)$$

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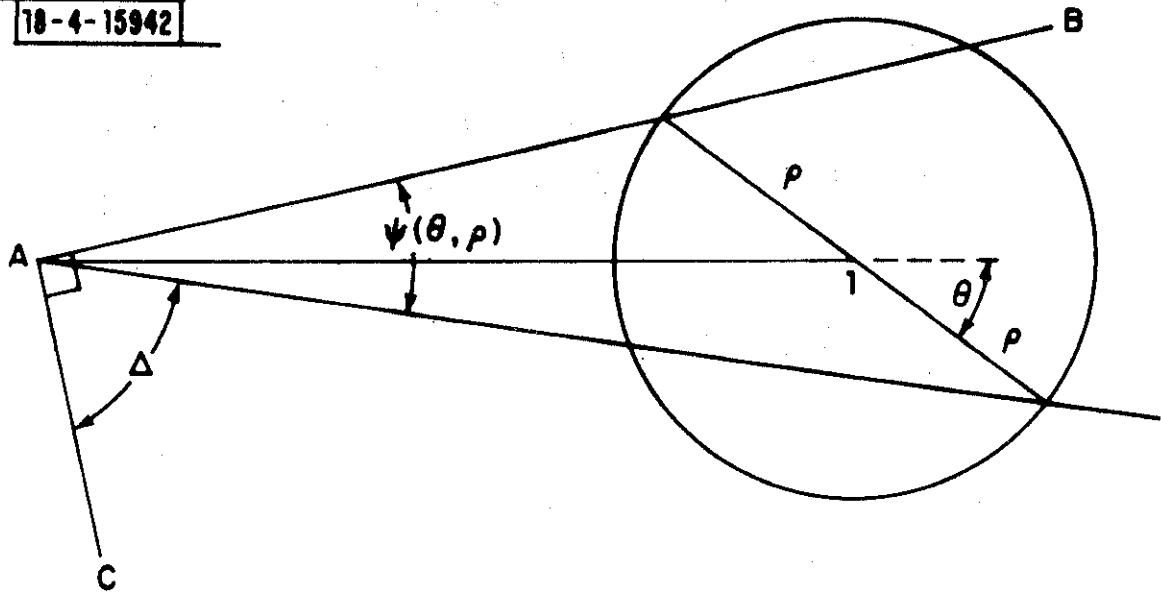


Fig. 3A. "Largest Value of  $\Delta$ " Which Yields No Error for Infinite  $E/N_0$  and  $-\pi \leq \theta < 0$ .

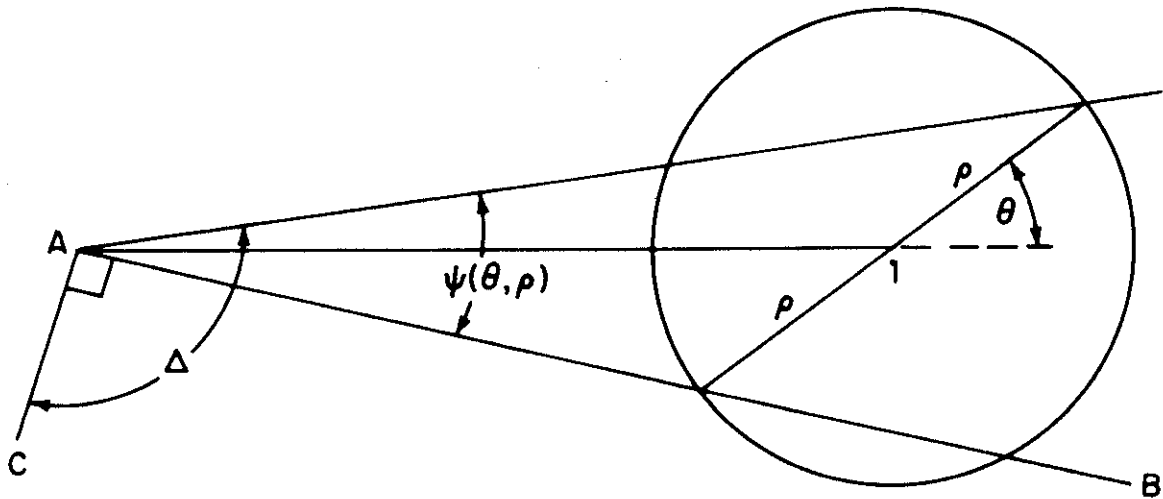


Fig. 3B.  $\Delta$  Must Be Greater Than  $\pi/2$  To Cause an Error at Infinite  $E/N_0$  with  $0 \leq \theta \leq \pi$ .

$\Delta_M$  is the largest value of  $\Delta$  for which  $P_{e|\theta}/\text{bit}$  is zero for all  $\theta$ . In Figure 4,  $\Delta_M$  vs.  $\rho$  is plotted. An acceptable range for  $\Delta$  is  $-\Delta_M \leq \Delta \leq \Delta_M$  for  $E/N_0$  infinite. For finite  $E/N_0$ , we would want to narrow the tolerance on  $\Delta$ .

We can also obtain  $P_{e|\Delta}/\text{bit}$  for infinite  $E/N_0$  for  $\Delta > \Delta_M$  since

$$P_{e|\Delta}/\text{bit} = \frac{1}{4} \Pr\{\Delta > \Delta_e(\theta, \rho)\} \quad \text{for } -\pi \leq \theta \leq 0 \quad (11)$$

The factor of 1/4 comes about from two factors of 1/2. The first is due to the fact that the cases  $\rho_2 = -\rho$  and  $\rho_2 = \rho$  are equally likely and only the former case leads to an error for  $\Delta < \pi/2$  and  $\theta < 0$ . The second is due to the fact that for positive  $\Delta$  we have an error only for  $\theta$  negative and  $\theta$  is equally likely to be positive as negative. Figure 5 shows a plot of  $\Delta_e(\theta, \rho)$  vs.  $\theta$  for  $\rho = 0.5$  and  $0.8$ . From this plot the  $E/N_0 = \infty$  curves in Figure 6 are derived.

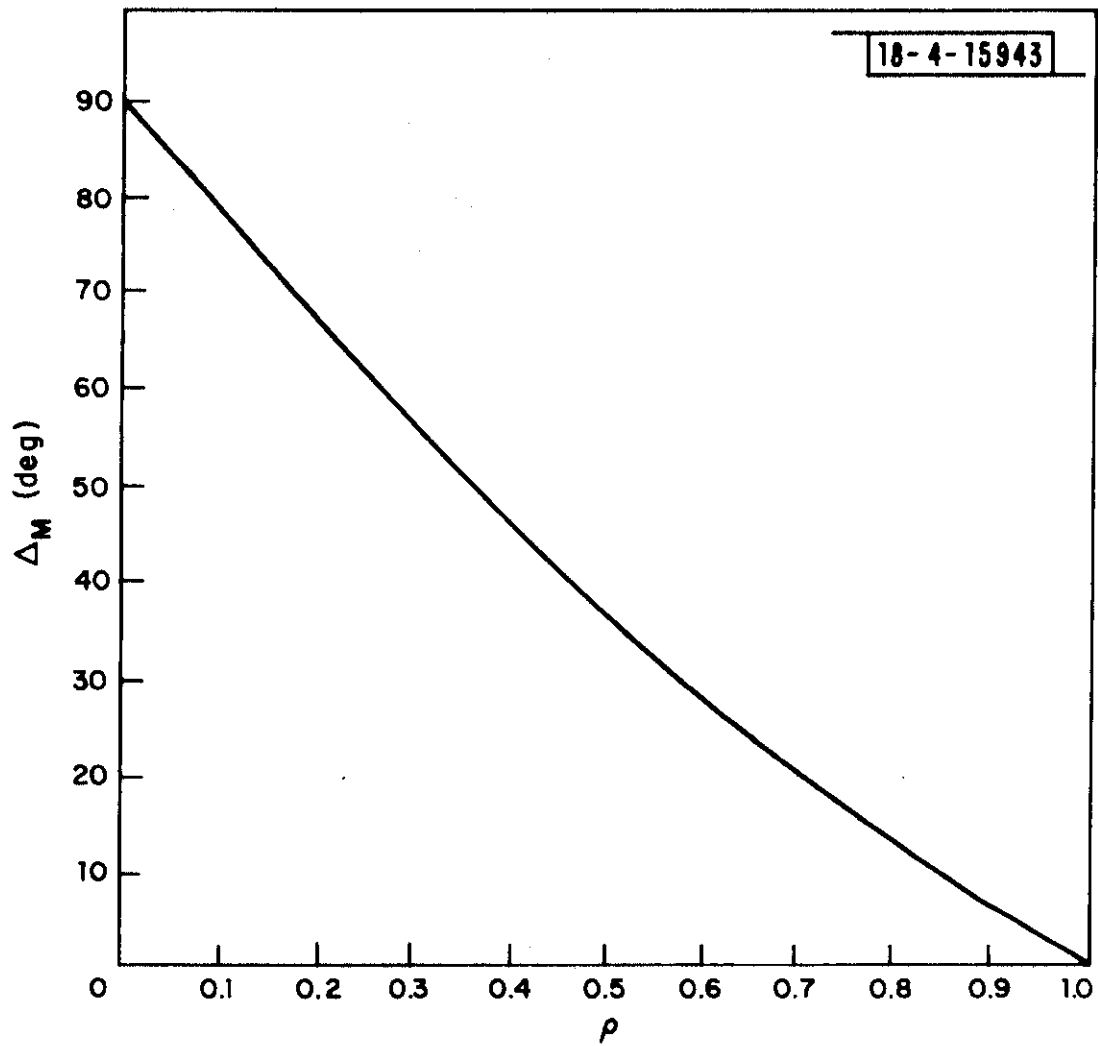


Fig. 4. Plot of  $\Delta_M$  vs  $\rho$ .



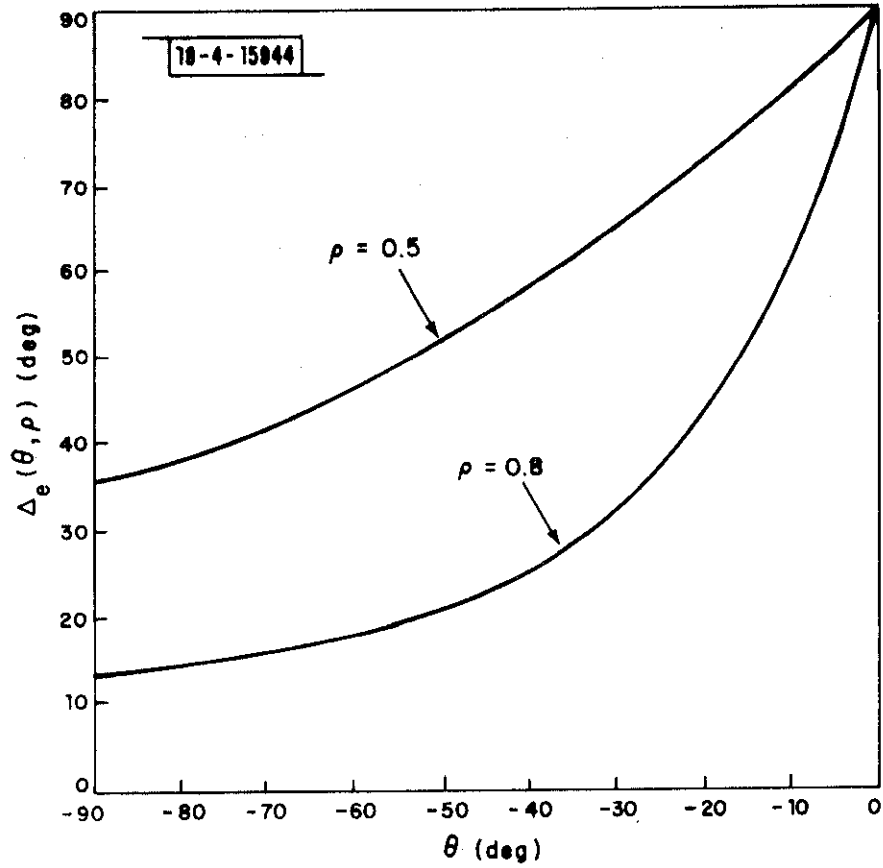


Fig. 5.  $\Delta_e(\theta, \rho)$  vs  $\theta$ .

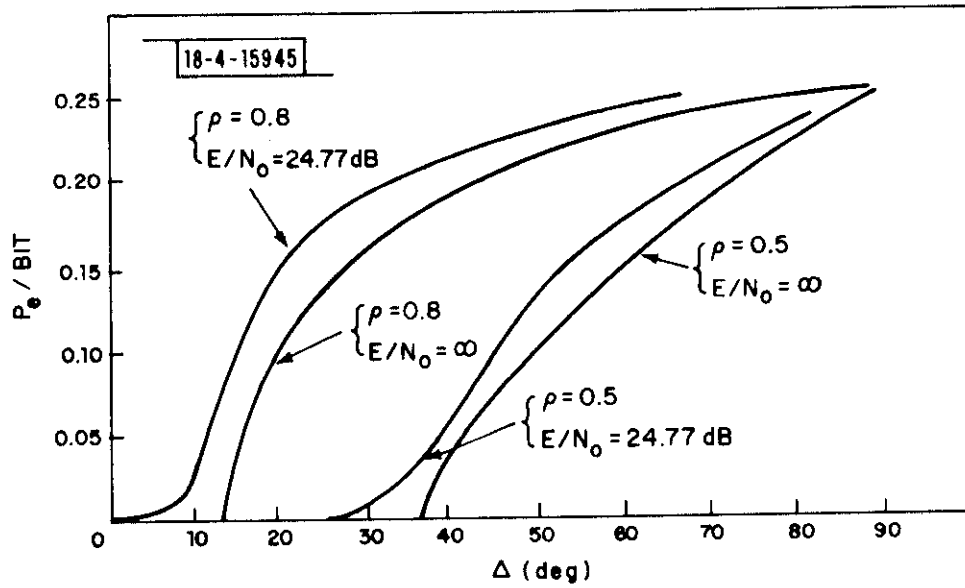


Fig. 6.  $P_e/\text{bit}$  vs  $\Delta$ .

## SECTION 4

### CONCLUSIONS

We have seen that the DPSK receiver can have large phase shifts and still yield negligible  $P_e/\text{bit}$  in the absence of interference. In interference, the situation is complicated and we attempt to summarize the results for  $E/N_0 \approx 25$  dB in Figure 7 and its accompanying table. The table gives combinations of  $\Delta$  and  $\rho$  which bracket  $P_e/\text{bit}$  of  $10^{-3}$ . The figure plots the percent of tolerance error which corresponds to a given  $\Delta$  vs IF carrier frequency. From the table, we can estimate an acceptable value of  $\Delta$  and from the figure convert  $\Delta$  to % tolerance necessary over the temperature range (nominally  $-20^\circ\text{C}$  to  $70^\circ\text{C}$ ) for a specific IF carrier frequency.

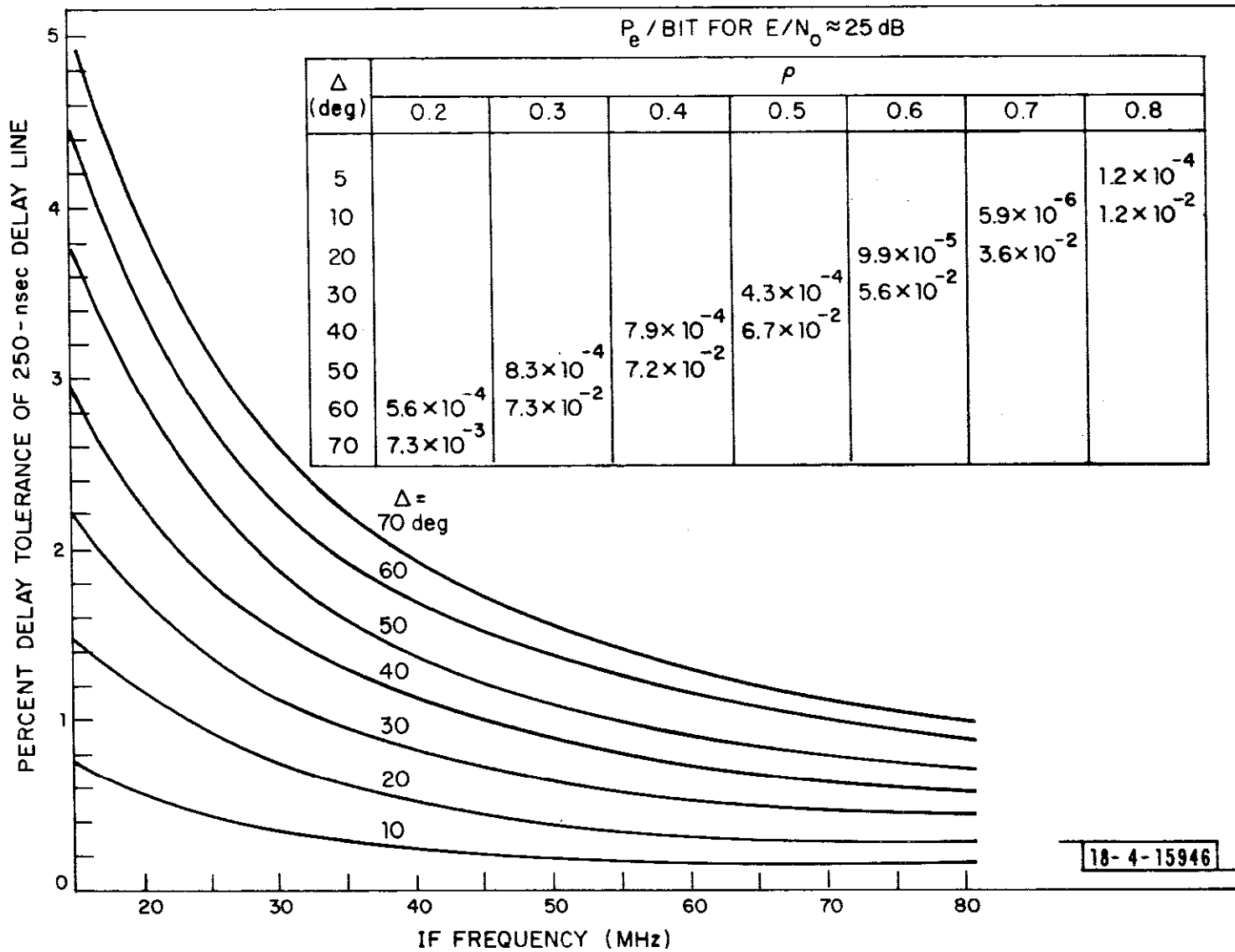


Fig. 7. Percent Tolerance of 250 nsec Delay Line vs IF Carrier Frequency.

APPENDIX A

DERIVATION OF  $P_e(E/N_0, \rho_1, \rho_2, \Delta, \theta)$

Applying the results of Stein [2,3] to our problem, we obtain for a given  $\theta$

$$P_{e/\theta}/\text{bit} = \frac{1}{2} [1 - Q(\sqrt{\beta}, \sqrt{\alpha}) + Q(\sqrt{\alpha}, \sqrt{\beta})] \quad (\text{A-1})$$

where

$$\begin{aligned} \alpha = \frac{1}{2} \frac{E}{N_0} & \left[ (1 + 2\rho_1 \cos \theta + \rho_1^2) + (1 + 2\rho_2 \cos \theta + \rho_2^2) \right. \\ & \left. - 2 \sqrt{(1 + 2\rho_1 \cos \theta + \rho_1^2)(1 + 2\rho_2 \cos \theta + \rho_2^2)} \cos(\psi + \Delta) \right] \end{aligned} \quad (\text{A-2})$$

and

$$\begin{aligned} \beta = \frac{1}{2} \frac{E}{N_0} & \left[ (1 + 2\rho_1 \cos \theta + \rho_1^2) + (1 + 2\rho_2 \cos \theta + \rho_2^2) \right. \\ & \left. + 2 \sqrt{(1 + 2\rho_1 \cos \theta + \rho_1^2)(1 + 2\rho_2 \cos \theta + \rho_2^2)} \cos(\psi + \Delta) \right]. \end{aligned} \quad (\text{A-3})$$

$\psi$  and  $\Delta$  are pictured in Figure A-1 where  $\psi$  is the angle between the resultant reference signal and the resultant information signal and  $\Delta$  is the phase offset. Since we have

$$\cos(\psi + \Delta) = \cos \psi \cos \Delta - \sin \psi \sin \Delta \quad (\text{A-4})$$

we must determine  $\cos \psi$  and  $\sin \psi$ . From Figure A-1 we see

$$x^2 + y^2 = \ell_2^2 \quad (\text{A-5})$$

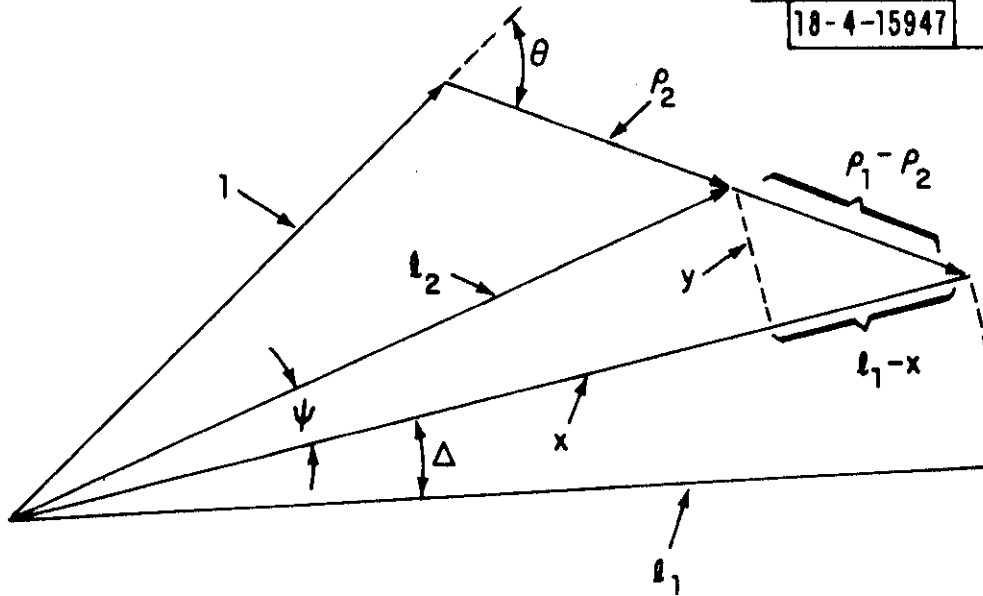
$$(\ell_1 - x)^2 + y^2 = (\rho_1 - \rho_2)^2 \quad (\text{A-6})$$

Combining (A-5) and (A-6) we obtain

$$x = \frac{\ell_1^2 + \ell_2^2 - (\rho_1 - \rho_2)^2}{2\ell_1} \quad (\text{A-7})$$

and

$$\begin{aligned} \cos \psi &= \frac{x}{\ell_2} = \frac{\ell_1^2 + \ell_2^2 - (\rho_1 - \rho_2)^2}{2\ell_1 \ell_2} \\ &= \frac{1 + (\rho_1 + \rho_2) \cos \theta + \rho_1 \rho_2}{\sqrt{(1 + 2\rho_1 \cos \theta + \rho_1^2)(1 + 2\rho_2 \cos \theta + \rho_2^2)}} \end{aligned} \quad (\text{A-8})$$



$$l_1 = \sqrt{1 + 2\rho_1 \cos \theta + \rho_1^2}$$

$$l_2 = \sqrt{1 + 2\rho_2 \cos \theta + \rho_2^2}$$

$$x^2 + y^2 = l_2^2$$

$$(l_1 - x)^2 + y^2 = (\rho_1 - \rho_2)^2$$

$$\cos \psi = \frac{x}{l_2} = \frac{l_1^2 + l_2^2 - (\rho_1 - \rho_2)^2}{2l_1 l_2}$$

Fig. A-1. Normalized ( $E/N_0 = 1$ ) Phasor Diagram for DPSK Receiver Output in Interferences  $\rho_1$  and  $\rho_2$ , with Phase Offset,  $\Delta$ .

From (A-8) we obtain  $\sin \psi$  as

$$\sin \psi = \frac{(\rho_1 - \rho_2) \sin \theta}{\sqrt{(1 + 2 \rho_1 \cos \theta + \rho_1^2)(1 + 2 \rho_2 \cos \theta + \rho_2^2)}} \quad (\text{A-9})$$

Substituting (A-8) and (A-9) into (A-4) and using the results in (A-2) and (A-3), we obtain Eqs. (5) and (6) respectively. The expressions can be simplified when  $\Delta = 0$ , since

$$\alpha = \frac{E}{2N_0} (\rho_1 - \rho_2)^2 \quad (\text{A-10})$$

and

$$\beta = \frac{E}{2N_0} \left[ 4 + 4 (\rho_1 + \rho_2) \cos \theta + (\rho_1 + \rho_2)^2 \right] \quad (\text{A-11})$$

When  $\Delta = 0$  and  $\rho_1 = \pm \rho_2$ , the error expressions simplify as follows:

If  $\rho_1 = \rho$ ,  $\rho_2 = -\rho$  then

$$P_e/\text{bit} = \frac{1}{2} [1 - Q(\sqrt{b}, \sqrt{a}) + Q(\sqrt{a}, \sqrt{b})] \quad (\text{A-12})$$

$$a = 2\rho^2 E/N_0 \quad (\text{A-13})$$

$$b = 2 E/N_0 \quad (\text{A-14})$$

and if  $\rho_2 = \rho_1 = \rho$  then

$$P_e/\text{bit} = \frac{1}{2} e^{-\frac{E}{N_0} (1 + \rho^2)} I_0\left(2\rho \frac{E}{N_0}\right) . \quad (\text{A-15})$$

A computer subroutine (Appendix B) has been written by Louise Balboni to evaluate Eq. (4). We can evaluate  $P_e/\text{bit}$  from Eq. (7) using this program or if appropriate (A-12) or (A-15).



APPENDIX B  
COMPUTER SUBROUTINE

```

SUBROUTINE CALPTH (PTH, ENO, RHO1, RHO2, DEL, THETA)
IMPLICIT REAL*8 (A-H, O-Z)
C COMPUTE COMMON TERMS
CDEL=DCOS (DEL)
TERM1=1.00+(RHO1+RHO2)*DCOS (THETA)
ATERM2=1.00-CDEL
BTERM2=1.00+CDEL
RHO1SQ=RHO1**2
RHO2SQ=RHO2**2
PRTERM=2.00*RHO1*RHO2*CDEL
ATERM3=(RHO1SQ-PRTERM+RHO2SQ)*.500
BTERM3=(RHO1SQ+PRTERM+RHO2SQ)*.500
TERM4=(RHO1-RHO2)*DSIN (DEL)*DSIN (THETA)
C COMPUTE A & B AS A COMBINATION OF THESE PREDEFINED TERMS
A=ENO*(TERM1*ATERM2+ATERM3+TERM4)
B=ENO*(TERM1*BTERM2+BTERM3-TERM4)
C ERROR PRINTOUT IN CASE OF NEGATIVE VALUE FOR SQRT FUNCTION
IF (A.LT.0.00.OR.B.LT.0.00)
1WRITE (6, 101) ENO, RHO1, RHO2, DEL, THETA, CDEL, TERM1, ATERM2, BTERM2, TERM3
1, TERM4, A, B
101 FORMAT (' ENO=', D12.5, ' RHO1=', D12.5, ' RHO2=', D12.5, ' DEL=', D12.5, '
1 THETA=', D12.5/' CDEL=', D12.5, ' TERM1=', D12.5, ' ATERM2=', D12.5, ' B
2TERM2=', D12.5, ' TERM3=', D12.5/' TERM4=', D12.5, ' A=', D12.5, ' B=', D1
32.5)
C COMPUTE ARGUMENTS FOR Q FUNCTION
SQRTA=DSQRT (A)
SQRTB=DSQRT (B)
C COMPUTE PTH
PTH=.500*(1.00-QFUNCT (SQRTB, SQRTA)+QFUNCT (SQRTA, SQRTB))
RETURN
END

```

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- [3] Schwartz, Mischa, Bennett, William R., and Stein, Seymour, Communication Systems and Techniques, (McGraw-Hill, Inc., N.Y.), 1965.